

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	A Dual-Loop PLL with DAC offset for Frequency Shift While Maintaining Input Tracking																																																																																																																																				
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/604730 </p> <p>EFS ID: 45498</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 30%;">Confirmation Code</th> <th style="width: 70%;">Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>1729</td> </tr> </tbody> </table> <p>First Named Applicant: Hide Hattori</p> <p>Attorney Docket Number: PS-93</p> <p>Timestamp: 2003-08-13 13:22:07 EDT</p> <p>From: US</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 25%;">Doc. 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application-body	fgPS93e2.TIF	6772	2003-08-13
application-body	fgPS93e3.TIF	10404	2003-08-13
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Total files size			609308

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